



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IFW

Applicant: Kenneth W. Marr et al.

Title: CIRCUITS AND METHODS TO PROTECT A GATE DIELECTRIC ANTIFUSE

Docket No.: 303.819US2

Serial No.: 10/770,800

Filed: February 3, 2004

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2814

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

☒ A return postcard.

☒ A Communication Concerning Related Applications (1 pg.).

☒ A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 4 cited documents.

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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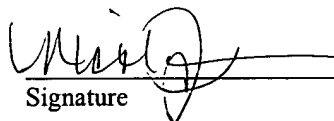
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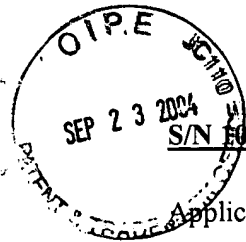
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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)



S/N 10/770,800

PATENT

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Applicant: Kenneth W. Marr et al. Examiner: Unknown
Serial No.: 10/770,800 Group Art Unit: 2814
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COMMUNICATION CONCERNING RELATED APPLICATIONS

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/652,429 6,630,724	August 31, 2000	303.674US1	GATE DIELECTRIC ANTIFUSE CIRCUITS AND METHODS FOR OPERATING SAME
10/231,756 6,751,150	August 29, 2002	303.819US1	CIRCUITS AND METHODS TO PROTECT A GATE DIELECTRIC ANTIFUSE
10/230,928	August 29, 2002	303.820US1	GATE DIELECTRIC ANTIFUSE CIRCUIT TO PROTECT A HIGH-VOLTAGE TRANSISTOR
10/680,481	October 6, 2003	303.674US2	GATE DIELECTRIC ANTIFUSE CIRCUITS AND METHODS FOR OPERATING SAME
10/931,366	August 31, 2004	303.820US2	GATE DIELECTRIC ANTIFUSE CIRCUIT TO PROTECT A HIGH-VOLTAGE TRANSISTOR

Respectfully submitted,
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